

IV Year – II SEMESTER

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CELLULAR AND MOBILE COMMUNICATIONS

UNIT I

CELLULAR MOBILE RADIO SYSTEMS: Introduction to Cellular Mobile System, Performance criteria, uniqueness of mobile radio environment, operation of cellular systems, Hexagonal shaped cells, Analog and Digital Cellular systems.

ELEMENTS OF CELLULAR RADIO SYSTEM DESIGN : General description of the problem, concept of frequency channels, Co-channel Interference Reduction Factor, desired C/I from a normal case in a Omni directional Antenna system, Cell splitting, consideration of the components of Cellular system.

UNIT II

INTERFERENCE : Introduction to Co-Channel Interference, real time Co-Channel interference, Co-Channel measurement, design of Antenna system, Antenna parameters and their effects, diversity receiver, non-cochannel interference-different types. **CELL COVERAGE FOR SIGNAL AND TRAFFIC:** Signal reflections in flat and hilly terrain, effect of human made structures, phase difference between direct and reflected paths, constant standard deviation, straight line path loss slope, general formula for mobile propagation over water and flat open area, near and long distance propagation antenna height gain, form of a point to point model.

UNIT III

CELL SITE AND MOBILE ANTENNAS : Sum and difference patterns and their synthesis, omni directional antennas, directional antennas for interference reduction, space diversity antennas, umbrella pattern antennas, minimum separation of cell site antennas, high gain antennas.

UNIT IV

FREQUENCY MANAGEMENT AND CHANNEL ASSIGNMENT: Numbering and grouping, setup access and paging channels channel assignments to cell sites and mobile units, channel sharing and borrowing, sectorization, overlaid cells, non fixed channel assignment.

UNIT V

Handoff, dropped calls and cell splitting, types of handoff, handoff invitation, delaying handoff, forced handoff, mobile assigned handoff. Intersystem handoff, cell splitting, micro cells, vehicle locating methods, dropped call rates and their evaluation.

UNIT VI

DIGITAL CELLULAR NETWORKS : GSM architecture, GSM channels, multiplex access scheme, TDMA, CDMA.

TEXTBOOKS :

1. Mobile Cellular Telecommunications – W.C.Y. Lee, Tata McGraw Hill, 2nd Edn., 2006.
2. Principles of Mobile Communications – Gordon L. Stuber, Springer International 2nd Edition, 2007.

REFERENCES :

1. Wireless Communications - Theodore. S. Rappoport, Pearson education, 2nd Edn., 2002.
2. Wireless and Mobile Communications – Lee McGraw Hills, 3rd Edition, 2006.
3. Mobile Cellular Communication – G Sasibhushana Rao Pearson
3. Wireless Communication and Networking – Jon W. Mark and Weihua Zhqung, PHI, 2005.
4. Wireless Communication Technology – R. Blake, Thompson Asia Pvt. Ltd., 2004.

IV Year – II SEMESTER

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ELECTRONIC MEASUREMENTS AND INSTRUMENTATION**UNIT I**

Performance characteristics of instruments, Static characteristics, Accuracy, Resolution, Precision, Expected value, Error, Sensitivity. Errors in Measurement, Dynamic Characteristics-speed of response, Fidelity, Lag and Dynamic error. DC Voltmeters- Multi-range, Range extension/Solid state and differential voltmeters, AC voltmeters- multi range, range extension, shunt. Thermocouple type RF ammeter, Ohmmeters series type, shunt type, Multi-meter for Voltage, Current and resistance measurements.

UNIT II

Signal Generator- fixed and variable, AF oscillators, Standard and AF sine and square wave signal generators, Function Generators, Square pulse, Random noise, sweep, Arbitrary waveform. Wave Analyzers, Harmonic Distortion Analyzers, Spectrum Analyzers, Digital Fourier Analyzers.

UNIT III

Oscilloscopes CRT features, vertical amplifiers, horizontal deflection system, sweep, trigger pulse, delay line, sync selector circuits, simple CRO, triggered sweep CRO, Dual beam CRO, . Dual trace oscilloscope, sampling oscilloscope, storage oscilloscope, digital readout oscilloscope, digital storage oscilloscope, Lissajous method of frequency measurement, standard specifications of CRO, probes for CRO- Active & Passive, attenuator type.

UNIT IV

AC Bridges Measurement of inductance- Maxwell's bridge, Anderson bridge. Measurement of capacitance -Schearing Bridge. Wheat stone bridge. Wien Bridge, Errors and precautions in using bridges. Q-meter.

UNIT V

Transducers- active & passive transducers : Resistance, Capacitance, inductance; Strain gauges, LVDT, Piezo Electric transducers, Resistance Thermometers, Thermocouples, Thermistors, Sensistors.

UNIT VI

Measurement of physical parameters force, pressure, velocity, humidity, moisture, speed, proximity and displacement. Data acquisition systems.

TEXTBOOKS :

1. Electronic instrumentation, second edition - H.S.Kalsi, Tata McGraw Hill, 2004.
2. Modern Electronic Instrumentation and Measurement Techniques – A.D. Helfrick and W.D. Cooper, PHI, 5th Edition, 2002.

REFERENCES :

1. Electronic Instrumentation & Measurements - David A. Bell, PHI, 2nd Edition, 2003.
2. Electronic Test Instruments, Analog and Digital Measurements - Robert A. Witte, Pearson Education, 2nd Ed., 2004.
3. Electronic Measurements & Instrumentations by K. Lal Kishore, Pearson Education - 2005.

OUTCOMES

The student will be able to

- Select the instrument to be used based on the requirements.
- Understand and analyze different signal generators and analyzers.
- Understand the design of oscilloscopes for different applications.
- Design different transducers for measurement of different parameters.

IV Year – II SEMESTER

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ELECTIVE – III

SATELLITE COMMUNICATIONS

UNIT I

INTRODUCTION : Origin of Satellite Communications, Historical Background, Basic Concepts of Satellite Communications, Frequency allocations for Satellite Services, Applications, Future Trends of Satellite Communications.

UNIT II

ORBITAL MECHANICS AND LAUNCHERS: Orbital Mechanics, Look Angle determination, Orbital perturbations, Orbit determination, launches and launch vehicles, Orbital effects in communication systems performance.

UNIT III

SATELLITE SUBSYSTEMS : Attitude and orbit control system, telemetry, tracking, Command and monitoring, power systems, communication subsystems, Satellite antenna Equipment reliability and Space qualification.

UNIT IV

SATELLITE LINK DESIGN : Basic transmission theory, system noise temperature and G/T ratio, Design of down links, up link design, Design of satellite links for specified C/N, System design example.

MULTIPLE ACCESS: Frequency division multiple access (FDMA) Intermodulation, Calculation of C/N. Time division Multiple Access (TDMA) Frame structure, Examples. Satellite Switched TDMA Onboard processing, DAMA, Code Division Multiple access (CDMA), Spread spectrum transmission and reception.

UNIT V

EARTH STATION TECHNOLOGY : Introduction, Transmitters, Receivers, Antennas, Tracking systems, Terrestrial interface, Primary power test methods.

LOW EARTH ORBIT AND GEO-STATIONARY SATELLITE SYSTEMS: Orbit consideration, coverage and frequency considerations,

Delay & Throughput considerations, System considerations, Operational NGSO constellation Designs

UNIT VI

SATELLITE NAVIGATION & THE GLOBAL POSITIONING SYSTEM [1] : Radio and Satellite Navigation, GPS Position Location principles, GPS Receivers and codes, Satellite signal acquisition, GPS Navigation Message, GPS signal levels, GPS receiver operation, GPS C/A code accuracy, Differential GPS.

TEXT BOOKS:

1. Satellite Communications – Timothy Pratt, Charles Bostian and Jeremy Allnutt, WSE, Wiley Publications, 2nd Edition, 2003.
2. Satellite Communications Engineering – Wilbur L. Pritchard, Robert A Nelson and Henri G.Suyderhoud, 2nd Edition, Pearson Publications, 2003.

REFERENCES :

1. Satellite Communications : Design Principles – M. Richharia, BS Publications, 2nd Edition, 2003.
2. Satellite Communication - D.C Agarwal, Khanna Publications, 5th Ed.
3. Fundamentals of Satellite Communications – K.N. Raja Rao, PHI, 2004
4. Satellite Communications – Dennis Roddy, McGraw Hill, 2nd Edition, 1996.

MIXED SIGNAL DESIGN

(ELECTIVE – III)

OBJECTIVES

The student will be introduced to

- Understand the Switched capacitors Circuits and Operation and Analysis, PLLS.
- In this course, students can study Data Converter Fundamentals, Nyquist Rate A/D Converters.
- Another main object of this course is to motivate the graduate students to study and to analyze the Oversampling Converters and Continuous-Time Filters.
- The concepts of Continuous-Time Filters, CMOS Transconductors Using Triode and Active Transistors and MOSFET-C Filters.

UNIT-I:

Switched Capacitor Circuits: Introduction to Switched Capacitor circuits-basic building blocks, Operation and Analysis, Non-ideal effects in switched capacitor circuits, Switched capacitor integrators first order filters, Switch sharing, biquad filters.

UNIT-II:

Phased Lock Loop (PLL): Basic PLL topology, Dynamics of simple PLL, Charge pump PLLs-Lock acquisition, Phase/Frequency detector and charge pump, Basic charge pump PLL, Non-ideal effects in PLLs-PFD/CP non-idealities, Jitter in PLLs, Delay locked loops, applications.

UNIT-III:

Data Converter Fundamentals: DC and dynamic specifications, Quantization noise, Nyquist rate D/A converters- Decoder based converters, Binary-Scaled converters, Thermometer-code converters, Hybrid converters

UNIT-IV:

Nyquist Rate A/D Converters: Successive approximation converters, Flash converter, Two-step A/D converters, Interpolating A/D converters, Folding A/D converters, Pipelined A/D converters, Time-interleaved converters.

UNIT-V:

Oversampling Converters: Noise shaping modulators, Decimating filters and interpolating filters, Higher order modulators, Delta sigma modulators with multi-bit quantizers, Delta sigma D/A

UNIT-VI:

Continuous-Time Filters: Introduction to Gm-C Filters, Bipolar Transconductors, CMOS transconductors Using Triode and Active Transistors, BiCMOS Transconductors, MOSFET-C Filters.

Text Books:

1. Design of Analog CMOS Integrated Circuits- Behzad Razavi, TMH Edition, 2002
2. Analog Integrated Circuit Design- David A. Johns, Ken Martin, Wiley Student Edition, 2013

Reference Books:

1. CMOS Mixed-Signal Circuit Design - R. Jacob Baker, Wiley Interscience, 2009.
2. CMOS Analog Circuit Design –Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.

OUTCOMES

After going through this course the student will be able to

- Understand the concepts of Switched Capacitor circuits.
- Design and analysis of Nyquist Rate A/D Converters.
- Extend the Mixed Signal Design to Different Applications.
- Concepts of Oversampling Converters and Continuous-Time Filters.

EMBEDDED SYSTEMS

(ELECTIVE – III)

OBJECTIVES

After going through this course the student will be able to

- Understand the building blocks of typical embedded system and different memory technology and memory types.
- Learn the characteristics of an embedded system, quality attributes of embedded systems, application specific and domain specific embedded system,
- Learn about communication devices and basics about VLSI and integrated circuit design and learn concept of firmware design approaches, ISR concept. Interrupt sources, interrupt servicing mechanism, multiple interrupts,
- Understand the concepts of c versus embedded c and compiler versus cross-compiler.
- Learn about the integrated development environment, software utility tool. Also learn about quality assurance and testing of the design, testing on host machine, simulators.

Unit-I:

Introduction: Embedded System-Definition, History, Classification, application areas and purpose of embedded systems, The typical embedded system-Core of the embedded system, Memory, Sensors and Actuators, Communication Interface, Embedded firmware, PCB and passive components. Characteristics, Quality attributes of an Embedded systems, Application-specific and Domain-Specific examples of an embedded system.

Unit-II:

Embedded Hardware Design: Analog and digital electronic components, I/O types and examples, Serial communication devices, Parallel device ports, Wireless devices, Timer and counting devices, Watchdog timer, Real time clock.

Unit-III:

Embedded Firmware Design: Embedded Firmware design approaches, Embedded Firmware development languages, ISR concept, Interrupt sources, Interrupt servicing mechanism, Multiple interrupts, DMA, Device driver programming, Concepts of C versus Embedded C and Compiler versus Cross-compiler.

Unit-IV:

Real Time Operating System: Operating system basics, Types of operating systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Threads, Processes and Scheduling, Task Scheduling, Communication, Synchronization, Device Drivers, How to choose an RTOS.

Hardware Software Co-Design: Fundamental Issues in Hardware Software Co-Design, Computational models in embedded design, Hardware software Trade-offs, Integration of Hardware and Firmware, ICE.

Unit-V:

Embedded System Development: The integrated development environment, Types of files generated on cross-compilation, Deassembler/Decompiler, Simulators, Emulators and Debugging, Target hardware debugging, Boundary Scan, Embedded Software development process and tools.

Unit-VI:

Embedded System Implementation And Testing: The main software utility tool, CAD and the hardware, Translation tools-Pre-processors, Interpreters, Compilers and Linkers, Debugging tools, Quality assurance and testing of the design, Testing on host machine, Simulators, Laboratory Tools.

Text Books:

1. Embedded Systems Architecture By Tammy Noergaard, Elsevier Publications, 2005
2. Embedded System Design, Frank Vahid, Tony Givargis, John Wiley Publications.

References:

1. Embedded Systems, Raj Kamal-Tata McGraw Hill Education Private Limited, Second Edition, 2008
2. Embedding system building blocks By Labrosse, CMP publishers.

OUTCOMES

After going through this course the student will be able to

- Know basics of embedded system, classification, memories, different communication interface and what embedded firmware is and its role in embedded system, different system components.
- Distinguish all communication devices in embedded system, other peripheral device.
- Distinguish concepts of C versus embedded C and compiler versus cross-compiler.
- Choose an operating system, and learn how to choose an RTOS

RF CIRCUIT DESIGN

(ELECTIVE – III)

UNIT -I:

Introduction to RF Electronics:

The Electromagnetic Spectrum, units and Physical Constants, Microwave bands – RF behavior of Passive components: Tuned resonant circuits, Vectors, Inductors and Capacitors - Voltage and Current in capacitor circuits – Tuned RF / IF Transformers.

UNIT -II:

Transmission Line Analysis: Examples of transmission lines- Transmission line equations and Biasing- Micro Strip Transmission Lines- Special Termination Conditions- sourced and Loaded Transmission Lines. **Single And Multiport Networks:** The Smith Chart, Interconnectivity networks, Network properties and Applications, Scattering Parameters.

UNIT -III:

Matching and Biasing Networks:

Impedance matching using discrete components – Micro strip line matching networks, Amplifier classes of Operation and Biasing networks.

UNIT-IV

RF Passive & Active Components: Filter Basics – Lumped filter design – Distributed Filter Design – Diplexer Filters- Crystal and Saw filters- Active Filters - Tunable filters – Power Combiners / Dividers – Directional Couplers – Hybrid Couplers – Isolators. RF Diodes – BJTs- FETs- HEMTs and Models.

UNIT -V:

RF Transistor Amplifier Design: Characteristics of Amplifiers - Amplifier Circuit Configurations, Amplifier Matching Basics, Distortion and noise products, Stability Considerations, Small Signal amplifier design, Power amplifier design, MMIC amplifiers, Broadband High Power multistage amplifiers, Low noise amplifiers, VGA Amplifiers.

UNIT -VI:

Oscillators: Oscillator basics, Low phase noise oscillator design, High frequency Oscillator configuration, LC Oscillators, VCOs, Crystal Oscillators, PLL Synthesizer, and Direct Digital Synthesizer. **RF Mixers:**

Basic characteristics of a mixer - Active mixers- Image Reject and Harmonic mixers, Frequency domain considerations.

TEXT BOOKS:

1. RF Circuit design: Theory and applications by Reinhold Ludwig, Pavel Bretchko. Pearson Education Asia Publication, New Delhi 2001.
2. Radio Frequency and Microwave Communication Circuits – Analysis and Design – Devendra K. Misra, Wiley Student Edition, John Wiley & Sons

REFERENCE BOOKS:

1. Radio frequency and Microwave Electronics - Mathew M.Radmangh, 2001, PE Asia Publ.
2. RF Circuit Design – Christopher Bowick, Cheryl Aljuni and John Biyler, Elsevier Science, 2008.
3. Secrets of RF Design - Joseph Carr., 3rd Edition, Tab Electronics.
4. Complete Wireless Design - Cotter W. Sawyer, 2nd Edition, Mc-Graw Hill.
5. Practical RF Circuit Design for Modem Wireless Systems Vol.2 -Less Besser and Rowan Gilmore.

Cloud Computing

(ELECTIVE – III)

Course Objectives: The student will learn about the cloud environment, building software systems and components that scale to millions of users in modern internet, cloud concepts capabilities across the various cloud service models including IaaS, PaaS, SaaS, and developing cloud based software applications on top of cloud platforms.

Course Outcomes:

1. Understanding the key dimensions of the challenge of Cloud Computing.
2. Assessment of the economics , financial, and technological implications for selecting cloud computing for own organization.
3. Assessing the financial, technological, and organizational capacity of employer's for actively initiating and installing cloud-based applications.
4. Assessment of own organizations' needs for capacity building and training in cloud computing-related IT areas.

Syllabus:

UNIT I: Systems modeling, Clustering and virtualization:

Scalable Computing over the Internet, Technologies for Network based systems, System models for Distributed and Cloud Computing, Software environments for distributed systems and clouds, Performance, Security And Energy Efficiency.

UNIT II: Virtual Machines and Virtualization of Clusters and Data Centers:

Implementation Levels of Virtualization, Virtualization Structures/ Tools and mechanisms, Virtualization of CPU, Memory and I/O Devices, Virtual Clusters and Resource Management, Virtualization for Data Center Automation.

UNIT III: Cloud Platform Architecture:

Cloud Computing and service Models, Architectural Design of Compute and Storage Clouds, Public Cloud Platforms, Inter Cloud Resource Management, Cloud Security and Trust Management. Service Oriented Architecture, Message Oriented Middleware.

UNIT IV: Cloud Programming and Software Environments:

Features of Cloud and Grid Platforms, Parallel & Distributed Programming Paradigms, Programming Support of Google App Engine, Programming on Amazon AWS and Microsoft Azure, Emerging Cloud Software Environments.

UNIT V: Cloud Resource Management and Scheduling:

Policies and Mechanisms for Resource Management Applications of Control Theory to Task Scheduling on a Cloud, Stability of a Two Level Resource Allocation Architecture, Feedback Control Based on Dynamic Thresholds. Coordination of Specialized Autonomic Performance Managers, Resource Bundling, Scheduling Algorithms for Computing Clouds, Fair Queuing, Start Time Fair Queuing, Borrowed Virtual Time, Cloud Scheduling Subject to Deadlines, Scheduling Map Reduce Applications Subject to Deadlines.

UNIT VI:

Storage Systems: Evolution of storage technology, storage models, file systems and database, distributed file systems, general parallel file systems. Google file system., Apache Hadoop, Big Table, Megastore, Amazon Simple Storage Service (S3) .

TEXT BOOKS:

1. Distributed and Cloud Computing, Kai Hwang, Geoffrey C. Fox, Jack J. Dongarra MK Elsevier.
2. Cloud Computing, Theory and Practice, Dan C Marinescu, MK Elsevier.
3. Cloud Computing, A Hands on approach, Arshadeep Bahga, Vijay Madiseti, University Press.

REFERENCE BOOK:

1. Cloud Computing, A Practical Approach, Anthony T Velte, Toby J Velte, Robert Elsenpeter, TMH.
2. Mastering Cloud Computing, Foundations and Application Programming, Raj Kumar Buyya, Christen vecctiola, S Tammarai selvi, TMH.

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ELECTIVE - IV**WIRELESS SENSORS AND NETWORKS****UNIT I****OVERVIEW OF WIRELESS SENSOR NETWORKS:**

Key definitions of sensor networks, Advantages of sensor Networks, Unique constraints and challenges, Driving Applications, Enabling Technologies for Wireless Sensor Networks.

ARCHITECTURES:

Single-Node Architecture - Hardware Components, Energy Consumption of Sensor Nodes, Operating Systems and Execution Environments, Network Architecture - Sensor Network Scenarios, Optimization Goals and Figures of Merit, Gateway Concepts.

UNIT II**NETWORKING Technologies:**

Physical Layer and Transceiver Design Considerations, Personal area networks (PANs), hidden node and exposed node problem, Topologies of PANs, MANETs, WANETs.

UNIT-III**MAC Protocols for Wireless Sensor Networks:**

Issues in Designing a MAC protocol for Ad Hoc Wireless Networks, Design goals of a MAC Protocol for Ad Hoc Wireless Networks, Classifications of MAC Protocols, Contention - Based Protocols, Contention - Based Protocols with reservation Mechanisms, Contention – Based MAC Protocols with Scheduling Mechanisms, MAC Protocols that use Directional Antennas, Other MAC Protocols.

UNIT-IV**ROUTING PROTOCOLS:**

Introduction, Issues in Designing a Routing Protocol for Ad Hoc Wireless Networks, Classification of Routing Protocols, Table –Driven Routing Protocols, On – Demand Routing Protocols, Hybrid Routing Protocols, Routing Protocols with Efficient Flooding Mechanisms, Hierarchical Routing Protocols, Power – Aware Routing Protocols, Proactive Routing.

UNIT-V**TRANSPORT LAYER AND SECURITY PROTOCOLS:**

Introduction, Issues in Designing a Transport Layer Protocol for Ad Hoc Wireless Networks, Design Goals of a Transport Layer Protocol for Ad Hoc Wireless Networks, Classification of Transport Layer Solutions, TCP Over Ad Hoc Wireless Networks, Other Transport Layer Protocol for Ad Hoc Wireless Networks.

UNIT- VI**SECURITY IN WSNs:**

Security in Ad Hoc Wireless Networks, Network Security Requirements, Issues and Challenges in Security Provisioning, Network Security Attacks, Key Management, Secure Routing in Ad Hoc Wireless Networks.

SENSOR NETWORK PLATFORMS AND TOOLS:

Sensor Node Hardware – Berkeley Motes, Programming Challenges, Node-level software platforms, Node-level Simulators, State-centric programming.

APPLICATIONS of WSN:

S Ultra wide band radio communication, Wireless fidelity systems. Future directions, Home automation, smart metering Applications.

TEXT BOOKS:

1. Ad Hoc Wireless Networks: Architectures and Protocols - C. Siva Ram Murthy and B.S.Manoj, 2004, PHI
2. Wireless Ad- hoc and Sensor Networks: Protocols, Performance and Control – Jagannathan Sarangapani, CRC Press
3. Holger Karl & Andreas Willig, “Protocols And Architectures for Wireless Sensor Networks”, John Wiley, 2005.

REFERENCES:

1. . Kazem Sohraby, Daniel Minoli, & Taieb Znati, “Wireless Sensor Networks- Technology, Protocols, and Applications”, John Wiley, 2007.
2. Feng Zhao & Leonidas J. Guibas, “Wireless Sensor Networks- An Information Processing Approach”, Elsevier, 2007.
3. Ad- Hoc Mobile Wireless Networks: Protocols & Systems, C.K. Toh ,1 ed. Pearson Education.
4. Wireless Sensor Networks - C. S. Raghavendra, Krishna M. Sivalingam, 2004, Springer.
5. Wireless Sensor Networks – S Anandamurugan , Lakshmi Publications

SYSTEM ON CHIP (ELECTIVE - IV)

OBJECTIVES

After going through this course the student will be able to

- Understand the System Architecture and Processor Architecture, approach for a SOC Design.
- Learn the, Basic concepts in Processor Micro Architecture, and Learn Different Types of Processors like VLIW Processors, Superscalar Processors etc.
- Learn about SOC external memory, Scratchpads and Cache memory and Multilevel Caches.
- Learn the SOC Design approach, Design and evaluation, Applications Like Image compression etc...

UNIT-I:

Introduction to the System Approach: System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory and Addressing. System level interconnection, an approach for SOC Design, System Architecture and Complexity.

UNIT-II:

Processors : Introduction , Processor Selection for SOC, Basic concepts in Processor Architecture, Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT-III:

Memory Design for SOC: Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – I, and D – Caches, Multilevel Caches, Virtual to real translation , SOC Memory System, Models of Simple Processor – memory interaction.

UNIT-IV:

Interconnect Customization and Configuration: Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses, Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time. SOC Customization: An overview, Customizing Instruction Processor

UNIT-V:

Interconnect Configuration: Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance- Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

UNIT-VI:

Application Studies / Case Studies: SOC Design approach, AES algorithms, Design and evaluation, Image compression – JPEG compression.

Text Books:

1. Computer System Design System-on-Chip - Michael J. Flynn and Wayne Luk, Wiley India Pvt. Ltd.
2. Design of System on a Chip: Devices and Components – Ricardo Reis, 1st Ed., 2004, Springer

Reference Books:

1. ARM System on Chip Architecture – Steve Furber –2nd Ed., 2000, Addison Wesley Professional.
2. System on Chip Verification – Methodologies and Techniques – Prakash Rashinkar, Peter Paterson and Leena Singh L, 2001, Kluwer Academic Publishers.

OUTCOMES

After going through this course the student will be able to

- Know basics of System Architecture and Processor Architecture.
- Know different Types of Processors Like VLIW Processors, Superscalar Processors etc. and Basic concepts in Processor Micro Architecture.
- Distinguish Cache memory and Multilevel Caches, SOC external memory.
- Know the Concept of Inter Connect Architectures, SOC Standard Buses and Reconfiguration Technologies.

LOW POWER VLSI DESIGN (ELECTIVE - IV)

OBJECTIVES

- The student will be able to understand the Fundamentals of Low Power VLSI Design.
- In this course, students can study low-Power Design Approaches, Power estimation and analysis.
- Another main object of this course is to motivate the graduate students to study and to analyze the Low-Voltage Low-Power Adders, Multipliers.
- The concepts of Low-Voltage Low-Power Memories and Future Trend and Development of DRAM.

UNIT-I:

Fundamentals of Low Power VLSI Design: Need for Low Power Circuit Design, Sources of Power Dissipation – Switching Power Dissipation, Short Circuit Power Dissipation, Leakage Power Dissipation, Glitching Power Dissipation, Short Channel Effects – Drain Induced Barrier Lowering and Punch Through, Surface Scattering, Velocity Saturation, Impact Ionization, Hot Electron Effect.

UNIT-II:

Low-Power Design Approaches:

Low-Power Design through Voltage Scaling: VTCMOS circuits, MTCMOS circuits, Architectural Level Approach – Pipelining and Parallel Processing Approaches.

Switched Capacitance Minimization Approaches: System Level Measures, Circuit Level Measures, Mask level Measures.

UNIT-III:

Power estimation and analysis: SPICE circuit simulators, gate level logic simulation, capacitive power estimation, static state power and gate level capacitance estimation.

UNIT-IV:

Low-Voltage Low-Power Adders: Introduction, Standard Adder Cells, CMOS Adder's Architectures – Ripple Carry Adders, Carry Look-Ahead Adders, Carry Select Adders, Carry Save Adders, Low-Voltage Low-Power

Design Techniques –Trends of Technology and Power Supply Voltage, Low-Voltage Low-Power Logic Styles.

UNIT-V:

Low-Voltage Low-Power Multipliers Introduction, Overview of Multiplication, Types of Multiplier Architectures, Braun Multiplier, Baugh-Wooley Multiplier, Booth Multiplier, Introduction to Wallace Tree Multiplier.

UNIT-VI:

Low-Voltage Low-Power Memories: Basics of ROM, Low-Power ROM Technology, Future Trend and Development of ROMs, Basics of SRAM, Memory Cell, Precharge and Equalization Circuit, Low-Power SRAM Technologies, Basics of DRAM, Self-Refresh Circuit, Future Trend and Development of DRAM.

Text Books:

1. Low-Voltage, Low-Power VLSI Subsystems – Kiat-Seng Yeo, Kaushik Roy, TMH Professional Engineering.

Reference Books:

1. Low Power CMOS VLSI Circuit Design – Kaushik Roy, Sharat C. Prasad, John Wiley & Sons, 2000.
2. Practical Low Power Digital VLSI Design – Gary K. Yeap, Kluwer Academic Press, 2002.

OUTCOMES

After going through this course the student will be able to

- Understand the concepts of Low-Power Design Approaches.
- Design and analysis of Low-Voltage Low-Power Circuits.
- Extend the Low Power Design to Different Applications.
- Understand of Low-Voltage Low-Power Memories and Basics of DRAM.

BIO-MEDICAL INSTRUMENTATION

(ELECTIVE - IV)

UNIT-I

Sources of Bioelectric potentials and Electrodes: Resisting and Action Potentials, Propagation of Action Potentials, The Bioelectric Potentials. Electrodes: Electrode theory, Bio Potential Electrodes, Biochemical Transducers, introduction to bio-medical signals.

UNIT-II

The Cardiovascular System: The Heart and Cardiovascular System, The Heart, Blood Pressure, Characteristics of Blood Flow, Heart Sounds, Cardio Vascular Measurements, Electrocardiography, Measurement of Blood Pressure, Measurement of Blood Flow and Cardiac output, Plethysmography, Measurement of Heart Sounds, Event detection, PQRS & T-Waves in ECG, the first & second Heart beats, ECG rhythm analysis, the di-crotic notch in the carotid pulse detection of events and waves, analysis of exercise ECG, analysis of event related potentials, correlation analysis of EEG channels, correlation of muscular contraction.

UNIT- III

Patient Care & Monitory and Measurements in Respiratory System: The elements of Intensive Care Monitory, Diagnosis, Calibration and reparability of Patient Monitoring equipment, other instrumentation for monitoring patients, pace makers, defibrillators, the physiology of respiratory system, tests and instrumentation for mechanics of breathing, respiratory theory equipment, analysis of respiration.

UNIT-IV

Bio telemetry and Instrumentation for the clinical laboratory Introduction to bio telemetry, Physiological parameters adaptable to bio telemetry, the components of bio telemetry system, implantable units, applications of telemetry in patient care – The blood, tests on blood cells, chemical test, automation of chemical tests.

UNIT-V

X-ray and radioisotope instrumentation and electrical safety of medical equipment: Generation of Ionizing radiation, instrumentation for diagnostic X-rays, special techniques, instrumentation for the medical use of

radioisotopes, radiation therapy - Physiological effects of electrical current, shock Hazards from electrical equipment, Methods of accident prevention.

UNIT-VI

Modern Imaging Systems: Tomography, Magnetic resonance Imaging System, Ultrasonic Imaging System, Medical Thermography.

TEXT BOOK:

1. Biomedical Instrumentation and Measurements – C. Cromwell, F.J. Weibell, E.A.Pfeiffer – Pearson education.
2. Biomedical signal analysis – Rangaraj, M. Rangayya – Wiley Inter science – John willey & Sons Inc.

Reference:

1. Hand Book of Bio-Medical Instrumentation – R.S. Khandpur, (TMH)
2. Introduction to Bio-Medical Engineering – Domach, (Pearson)
3. Introduction to Bio-Medical Equipment Technology – Cart, (Pearson)

EMI / EMC

Pre requisites: EMTL and AWP Courses.

Objectives:

- Student shall be able to understand the root causes for Electromagnetic Noise (EMI), its sources.
- Shall be able to understand the effects of EMI and the required precautions to be taken/to be discussed with his peer group.
- Shall be able to understand the different measurement techniques of EMI (for conducted and normal) and their influences in detail.
- Shall be able to understand different compatibility techniques (EMC) to reduce/suppress EMI.
- Shall be able to understand different standards being followed across the world in the fields of EMI/EMC.

UNIT-I: Natural and Nuclear sources of EMI / EMC : Introduction, Electromagnetic environment, History, Concepts, Practical experiences and concerns, frequency spectrum conservations. An overview of EMI / EMC, Natural and Nuclear sources of EMI.

UNIT-II: EMI from apparatus, circuits and open area test sites : Electromagnetic emissions, noise from relays and switches, non-linearities in circuits, passive inter modulation, cross talk in transmission lines, transients in power supply lines, electromagnetic interference (EMI). Open area test sites and measurements.

UNIT-III: Radiated and conducted interference measurements: Anechoic chamber, TEM cell, GH TEM Cell, characterization of conduction currents / voltages, conducted EM noise on power lines, conducted EMI from equipment, Immunity to conducted EMI detectors and measurements.

UNIT-IV: ESD, Grounding, shielding, bonding and EMI filters : Principles and types of grounding, shielding and bonding, characterization of filters, power lines filter design. ESD, Electrical fast transients / bursts, electrical surges.

UNIT-V: Cables, connectors, components: Introduction, EMI suppression cables, EMC connectors, EMC gaskets, Isolation transformers, opto-isolators, Transient and Surge Suppression Devices.

UNIT-VI: EMC standards- National / International :: Introduction, Standards for EMI and EMC, MIL-Standards, IEEE/ANSI standards, CISPR/IEC standards, FCC regulations, Euro norms, British Standards, EMI/EMC standards in JAPAN, Conclusions.

Text Books :

1. Engineering Electromagnetic Compatibility by **Dr. V.P. Kodali, IEEE Publication**, Printed in India by **S. Chand & Co. Ltd., New Delhi, 2000.**
2. Electromagnetic Interference and Compatibility **IMPACT series, IIT – Delhi, Modules 1 – 9.**

References :

1. Introduction to Electromagnetic Compatibility, NY, **John Wiley, 1992,** by **C.R. Pal.**

Outcomes-

At the end of this Course

- Students shall be able to distinguish effects of EMI and counter measures by EMC-techniques.
- Students shall apply the knowledge gained in selecting proper gadget/device/appliance/system, as per EMC- norms specified by regulating authorities.
- Students shall choose career in the fields of EMI/EMC as an Engineer/Researcher/Entrepreneur in India/abroad.

IV Year – II SEMESTER

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Project & Seminar