III Year – I SEMESTER

	T	P	C
	3+1	0	3

PULSE AND DIGITAL CIRCUITS

OBJECTIVES

The student will be made

- To understand the concept of wave shaping circuits, Switching Characteristics of diode and transistor.
- To analyze different types of Multi vibrators and their design procedures.
- To Introduce to Time-base Generators and Principles of Synchronization and Frequency division.
- To Understand Sampling Gates and to Design NAND and NOR gates using various logic families.

UNIT I

LINEAR WAVE SHAPING: High pass, low pass RC circuits, their response for sinusoidal, step, pulse, square and ramp inputs. RC network as differentiator and integrator, attenuators, its applications in CRO probe, RL and RLC circuits and their response for step input, Ringing circuit.

UNIT II

NON-LINEAR WAVE SHAPING : Diode clippers, Transistor clippers, clipping at two independent levels, Transfer characteristics of clippers, Emitter coupled clipper, Comparators, applications of voltage comparators, clamping operation, clamping circuits using diode with different inputs, Clamping circuit theorem, practical clamping circuits, effect of diode characteristics on clamping voltage, Transfer characteristics of clampers.

UNIT III

SWITCHING CHARACTERISTICS OF DEVICES : Diode as a switch, piecewise linear diode characteristics, Transistor as a switch, Break down voltage consideration of transistor, saturation parameters of Transistor and their variation with temperature, Design of transistor switch, transistor-switching times.

Digital Logic gate circuits: Realization of Logic Gates using DTL, TTL, ECL and CMOS logic circuits, Comparison of logic families.

UNIT IV

MULTIVIBRATORS :

Bistable Multi Vibrator: Analysis and Design of Fixed Bias, Self Bias Bistable Multi Vibrator, Collector catching Diodes, Commutating Capacitors,

Methods of Triggering using RC network & Diode, Emitter Coupled Bistable Multi Vibrator (Schmitt trigger).

Monostable Multi Vibrator: Analysis and Design of Collector Coupled Monostable Multi Vibrator, Triggering method of a Monostable Multi Vibrator, Application of Monostable Multi Vibrator as a Voltage to Time Converter.

Astable Multi Vibrator: Analysis and Design of Collector Coupled Astable Multi vibrator , Application of Astable Multi Vibrator as a Voltage to Frequency Converter. All circuits are transistor version.

UNIT V
VOLTAGE TIME BASE GENERATORS : General features of a time base signal, methods of generating time base waveform, Miller and Bootstrap time base generators – basic principles, Transistor miller time base generator, Transistor Bootstrap time base generator.

UNIT VI
SYNCHRONIZATION AND FREQUENCY DIVISION & SAMPLING GATES : Principles of Synchronization, Frequency division in sweep circuit, Astable relaxation circuits, Monostable relaxation circuits, Synchronization of a sweep circuit with symmetrical signals.

Basic operating principles of sampling gates, Unidirectional and Bi-directional sampling gates, Reduction of pedestal in gate circuits, Applications of sampling gates.

TEXT BOOKS :
1. Pulse, Digital and Switching Waveforms - J. Millman and H. Taub, McGraw-Hill, 1991.
2. Solid State Pulse circuits - David A. Bell, PHI, 4th Edn., 2002 .

REFERENCES :
1. Pulse and Digital Circuits – A. Anand Kumar, PHI, 2005.
2. Wave Generation and Shaping - L. Strauss.
3. Pulse, Digital Circuits and Computer Fundamentals - R.Venkataraman.

OUTCOMES
After going through this course the student will be able to
- Design linear and non-linear wave shaping circuits.
- Apply the fundamental concepts of wave shaping for various switching and signal generating circuits.
- Design different multivibrators and time base generators.

	T	**P**	**C**
III Year – I SEMESTER	**3+1**	**0**	**3**

LINEAR IC APPLICATIONS

OBJECTIVES
The student will
- Study characteristics, realize circuits, design for signal analysis using Op-amp ICs.
- Study the linear and non-linear applications of operational amplifiers.
- Study IC 555 timer, PLL and VCO with their applications.
- Study and understand different types of ADCs and DACs
- Acquire skills required for designing and testing integrated circuits

UNIT I
INTEGRATED CIRCUITS: Differential Amplifier- DC and AC analysis of Dual input Balanced output Configuration, Properties of other differential amplifier configuration (Dual Input Unbalanced Output, Single Ended Input – Balanced/ Unbalanced Output), DC Coupling and Cascade Differential Amplifier Stages, Level translator.

UNIT II
Characteristics of OP-Amps, Integrated circuits-Types, Classification, Package Types and Temperature ranges, Power supplies, Op-amp Block Diagram, ideal and practical Op-amp Specifications, DC and AC characteristics, 741 op-amp & its features, FET input. Op-Amps, Op-Amp parameters & Measurement, Input & Out put Off set voltages & currents, slew rates, CMRR, PSRR, drift, Frequency Compensation technique.

UNIT III
LINEAR and NON-LINEAR APPLICATIONS OF OP- AMPS: Inverting and Non-inverting amplifier, Integrator and differentiator, Difference amplifier, Instrumentation amplifier, AC amplifier, V to I, I to V converters, Buffers. Non- Linear function generation, Comparators, Multivibrators, Triangular and Square wave generators, Log and Anti log Amplifiers, Precision rectifiers.

UNIT IV
ACTIVE FILTERS, ANALOG MULTIPLIERS AND MODULATORS: Introduction, Butter worth filters – 1st order, 2nd order LPF, HPF filters. Band pass, Band reject and All pass filters.

Four Quadrant multiplier, balanced modulator, IC1496,Applications of analog switches and Multiplexers, Sample & Hold amplifiers.

UNIT V
TIMERS & PHASE LOCKED LOOPS: Introduction to 555 timer, functional diagram, Monostable and Astable operations and applications, Schmitt Trigger. PLL - introduction, block schematic, principles and description of individual blocks, 565 PLL, Applications of PLL – frequency multiplication, frequency translation, AM, FM & FSK demodulators. Applications of VCO (566).

UNIT VI
DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERTERS : Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, and IC 1408 DAC, Different types of ADCs – parallel Comparator type ADC, counter type ADC, successive approximation ADC and dual slope ADC.DAC and ADC Specifications, Specifications AD 574 (12 bit ADC).

TEXT BOOKS :
1. Linear Integrated Circuits – D. Roy Chowdhury, New Age International (p) Ltd, 2nd Edition,2003.
2. Op-Amps & Linear ICs - Ramakanth A. Gayakwad, PHI,1987.

REFERENCES :
1. Design with Operational Amplifiers & Analog Integrated Circuits - Sergio Franco, McGraw Hill, 1988.
2. OP AMPS and Linear Integrated Circuits concepts and Applications, James M Fiore, Cenage Learning India Ltd.
3. Operational Amplifiers & Linear Integrated Circuits–R.F.Coughlin & Fredrick Driscoll, PHI, 6th Edition.
4. Operational Amplifiers – C.G. Clayton, Butterworth & Company Publ.Ltd./ Elsevier, 1971.
5. Operational Amplifiers & Linear ICs – David A Bell, Oxford Uni. Press, 3rd Edition.

OUTCOMES
After going through this course the student will be able to
- Design circuits using operational amplifiers for various applications.
- Analyze and design amplifiers and active filters using Op-amp.
- Acquire skills required for designing and testing integrated circuits
- Understand the gain-bandwidth concept and frequency response of the three basic amplifiers. Understand thoroughly the operational amplifiers with linear integrated circuits.
- Design combinational logic circuits for different applications.

	T	**P**	**C**
III Year – I SEMESTER	**3+1**	**0**	**3**

CONTROL SYSTEMS

OBJECTIVES

The student will

- Learn the fundamental concepts of Control systems and mathematical modelling of the system.
- Study the concepts of time response and frequency response of the system.
- Understand the basics of stability analysis of the system.

UNIT I
INTRODUCTION

Concepts of Control Systems- Open Loop and closed loop control systems and their differences- Different examples of control systems- Classification of control systems, Feed-Back Characteristics, Effects of feedback. Mathematical models – Differential equations, Impulse Response and transfer functions - Translational and Rotational mechanical systems

UNIT II
TRANSFER FUNCTION REPRESENTATION

Transfer Function of DC Servo motor - AC Servo motor- Synchro transmitter and Receiver, Block diagram representation of systems considering electrical systems as examples -Block diagram algebra – Representation by Signal flow graph - Reduction using mason's gain formula.

UNIT III
TIME RESPONSE ANALYSIS

Standard test signals - Time response of first order systems – Characteristic Equation of Feedback control systems, Transient response of second order systems - Time domain specifications – Steady state response - Steady state errors and error constants – Effects of proportional derivative, proportional integral systems.

UNIT IV
STABILITY ANALYSIS IN S-DOMAIN

The concept of stability – Routh's stability criterion – qualitative stability and

conditional stability – limitations of Routh's stability.

Root Locus Technique:
The root locus concept - construction of root loci-effects of adding poles and zeros to G(s)H(s) on the root loci.

UNIT V
FREQUENCY RESPONSE ANALYSIS
Introduction, Frequency domain specifications-Bode diagrams-Determination of Frequency domain specifications and transfer function from the Bode Diagram-Phase margin and Gain margin-Stability Analysis from Bode Plots.
STABILITY ANALYSIS IN FREQUENCY DOMAIN:
Polar Plots, Nyquist Plots Stability Analysis.

UNIT VI
CLASSICAL CONTROL DESIGN TECHNIQUES
Compensation techniques – Lag, Lead, Lead-Lag Controllers design in frequency Domain, PID Controllers. State Space Analysis of Continuous Systems Concepts of state, state variables and state model, derivation of state models from block diagrams, Diagonalization- Solving the Time invariant state Equations- State Transition Matrix and it's Properties – Concepts of Controllability and Observability.

TEXT BOOKS:
1. Automatic Control Systems 8th edition– by B. C. Kuo 2003– John wiley and son's.,
2. Control Systems Engineering – by I. J. Nagrath and M. Gopal, New Age International (P) Limited, Publishers, 2nd edition.

REFERENCE BOOKS:
1. Modern Control Engineering – by Katsuhiko Ogata – Prentice Hall of India Pvt. Ltd., 3rd edition, 1998.
2. Control Systems by N.K.Sinha, New Age International (P) Limited Publishers, 3rd Edition, 1998.

OUTCOMES
After going through this course the student will be able to

- Represent the mathematical model of a system.
- Determine the response of different order systems for various step inputs.
- Analyse the stability of the system.

III Year – I SEMESTER

T	P	C
3+1	0	3

DIGITAL SYSTEM DESIGN & DIGITAL IC APPLICATIONS

OBJECTIVES
The student will be introduced to
- The electrical behavior of CMOS both in static and dynamic conditions and before that study the diode/transistor-transistor logic and Emitter coupled logic.
- In this course, students can study Integrated circuits for all digital operational designs like adder, subtractor, multipliers, multiplexers, registers, counters, flip flops, encoders, decoders and memory elements like RAM and ROM.
- Design and to develop the internal circuits for different digital operations and simulate them using hardware languages using integrated circuits.
- Understand the concepts of SSI Latches and Flip-Flops and Design of Counters using Digital ICs, modeling of sequential logic integrated circuits using VHDL.

Unit-I:
Digital Design Using HDL: Design flow, program structure, History of VHDL, VHDL requirements, Levels of Abstraction, Elements of VHDL, Concurrent and Sequential Statements, Packages, Libraries and Bindings, Objects and Classes, Subprograms, Comparison of VHDL and Verilog HDL.

Unit-II:
VHDL Modelling : Simulation, Logic Synthesis, Inside a logic Synthesizer, Constraints, Technology Libraries, VHDL and Logic Synthesis, Functional Gate-Level verification, Place and Route, Post Layout Timing Simulation, Static Timing, Major Netlist formats for design representation, VHDL Synthesis-Programming Approach.

Unit-III:
Programmable Logic Devices (PLDs) & Memories: Programmable Read Only Memory, Programmable Logic Array, Programmable Array Logic Devices, ROM: Internal structure, 2D-Decoding, Commercial ROM types, timing and applications,. Static RAM: Internal structure, SRAM timing, standard, synchronous SRAMS, Dynamic RAM: Internal structure, timing, synchronous DRAMs. Design considerations of PLDs with relevant Digital ICs.

Unit-IV:
Digital Logic Families and Interfacing: Introduction to logic families, CMOS logic, CMOS steady state and dynamic electrical behavior, CMOS

logic families.bipolar logic, transistor-transistor logic, TTL families, CMOS/TTL interfacing, low voltage CMOS logic and interfacing, Emitter coupled logic.

Unit-V:

Combinational Logic Design: Adders & Subtractors, Ripple Adder, Look Ahead Carry Generator, Binary Parallel Adder, Binary Adder-Subtractor, ALU, Decoders, encoders, three state devices, multiplexers and demultiplexers, Code Converters, parity circuits, comparators, multipliers, Barrel Shifter, Simple Floating-Point Encoder, Cascading Comparators, Dual Priority Encoder, Design considerations with relevant Digital ICs, modeling of Circuits by using VHDL.

Unit-VI:

Sequential Logic Design: SSI Latches and Flip-Flops, Counters, Design of Counters using Digital ICs, Ring Counter, Johnson Counter, Modulus N Synchronous Counters, MSI Registers, Shift Registers, Modes of Operation of Shift Registers, Universal Shift Registers, MSI Shift Registers, Design considerations with relevant Digital ICs, modeling of circuits by using VHDL.
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TEXT BOOKS:
1. Digital Design Principles & Practices – John F.Wakerly, PHI/ Pearson Education Asia, 3rd Edition, 2005.
2. Designing with TTL Integrated Circuits: Robert L. / John R. Morris & Miller.

REFERENCES:
1. "Fundamentals of Digital logic design with VHDL". Stephen Brown & Zvonko Vranesic, Tata McGraw Hill, 2nd edition.
2. VHDL Primer – J. Bhasker, Pearson Education/ PHI, 3rd Edition.

OUTCOMES:
After going through this course the student will be able to
- Understand the concepts of different logics and implementations using Integrated circuits.
- Design and analyze any Digital design in real time applications.
- Extend the digital operations to any width by connecting the ICs and can also design, simulate their results using hardware description language.
- Understand the concepts of MSI Registers and Modes of Operation of Shift Registers, Universal Shift Registers.

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III Year – I SEMESTER	**3+1**	**0**	**3**

ANTENNAS AND WAVE PROPAGATION

OBJECTIVES

The student will be able to

- understand the applications of the electromagnetic waves in free space.
- introduce the working principles of various types of antennas
- discuss the major applications of antennas with an emphasis on how antennas are employed to meet electronic system requirements.
- understand the concepts of radio wave propagation in the atmosphere.

UNIT I

ANTENNA FUNDAMENTALS: Introduction, Radiation Mechanism – single wire, 2 wire, dipoles, Current Distribution on a thin wire antenna. Antenna Parameters - Radiation Patterns, Patterns in Principal Planes, Main Lobe and Side Lobes, Beamwidths, Polarization, Beam Area, Radiation Intensity, Beam Efficiency, Directivity, Gain and Resolution, Antenna Apertures, Aperture Efficiency, Effective Height, illustrated Problems.

UNIT II

THIN LINEAR WIRE ANTENNAS: Retarded Potentials, Radiation from Small Electric Dipole, Quarter wave Monopole and Half wave Dipole – Current Distributions, Evaluation of Field Components, Power Radiated, Radiation Resistance, Beamwidths, Directivity, Effective Area and Effective Height. Natural current distributions, fields and patterns of Thin Linear Center-fed Antennas of different lengths, Radiation Resistance at a point which is not current maximum. Antenna Theorems – Applicability and Proofs for equivalence of directional characteristics, Loop Antennas: Small Loops - Field Components, Comparison of far fields of small loop and short dipole, Concept of short magnetic dipole, D and R_r relations for small loops.

UNIT III

ANTENNA ARRAYS : 2 element arrays – different cases, Principle of Pattern Multiplication, N element Uniform Linear Arrays – Broadside, End-fire Arrays, EFA with Increased Directivity, Derivation of their characteristics and comparison; Concept of Scanning Arrays. Directivity

Relations (no derivations). Related Problems. Binomial Arrays, Effects of Uniform and Non-uniform Amplitude Distributions, Design Relations. Arrays with Parasitic Elements, Yagi-Uda Arrays, Folded Dipoles and their characteristics.

UNIT IV

NON-RESONANT RADIATORS : Introduction, Traveling wave radiators – basic concepts, Long wire antennas – field strength calculations and patterns, Microstrip Antennas-Introduction, Features, Advantages and Limitations, Rectangular Patch Antennas –Geometry and Parameters, Impact of different parameters on characteristics. Broadband Antennas: Helical Antennas – Significance, Geometry, basic properties; Design considerations for monofilar helical antennas in Axial Mode and Normal Modes (Qualitative Treatment).

UNIT V

VHF, UHF AND MICROWAVE ANTENNAS : Reflector Antennas : Flat Sheet and Corner Reflectors. Paraboloidal Reflectors – Geometry, characteristics, types of feeds, F/D Ratio, Spill Over, Back Lobes, Aperture Blocking, Off-set Feeds, Cassegrain Feeds.

Horn Antennas – Types, Optimum Horns, Design Characteristics of Pyramidal Horns; Lens Antennas – Geometry, Features, Dielectric Lenses and Zoning, Applications, Antenna Measurements – Patterns Required, Set Up, Distance Criterion, Directivity and Gain Measurements (Comparison, Absolute and 3-Antenna Methods).

UNIT VI

WAVE PROPAGATION : Concepts of Propagation – frequency ranges and types of propagations. Ground Wave Propagation–Characteristics, Parameters, Wave Tilt, Flat and Spherical Earth Considerations. Sky Wave Propagation – Formation of Ionospheric Layers and their Characteristics, Mechanism of Reflection and Refraction, Critical Frequency, MUF and Skip Distance – Calculations for flat and spherical earth cases, Optimum Frequency, LUHF, Virtual Height, Ionospheric Abnormalities, Ionospheric Absorption.

Fundamental Equation for Free-Space Propagation, Basic Transmission Loss Calculations. Space Wave Propagation – Mechanism, LOS and Radio Horizon. Tropospheric Wave Propagation – Radius of Curvature of path, Effective Earth's Radius, Effect of Earth's Curvature, Field Strength Calculations, M-curves and Duct Propagation, Tropospheric Scattering.

TEXT BOOKS

1. Antennas for All Applications – John D. Kraus and Ronald J. Marhefka, 3rd Edition, TMH, 2003.
2. Electromagnetic Waves and Radiating Systems – E.C. Jordan and K.G. Balmain, PHI, 2nd Edition, 2000.

REFERENCES

1. Antenna Theory - C.A. Balanis, John Wiley and Sons, 2nd Edition, 2001.
2. Antennas and Wave Propagation – K.D. Prasad, Satya Prakashan, Tech India Publications, New Delhi, 2001.
3. Transmission and Propagation – E.V.D. Glazier and H.R.L. Lamont, The Services Text Book of Radio, vol. 5, Standard Publishers Distributors, Delhi.
4. Electronic and Radio Engineering – F.E. Terman, McGraw-Hill, 4th Edition, 1955.
5. Antennas – John D. Kraus, McGraw-Hill, 2nd Edition, 1988.

OUTCOMES

After going through this course the student will be able to

- Identify basic antenna parameters.
- Design and analyze wire antennas, loop antennas, reflector antennas, lens antennas, horn antennas and microstrip antennas
- Quantify the fields radiated by various types of antennas
- Design and analyze antenna arrays
- Analyze antenna measurements to assess antenna's performance
- Identify the characteristics of radio wave propagation

III Year – I SEMESTER

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Pulse & Digital Circuits Lab

1.   Linear wave shaping.

2.   Non Linear wave shaping – Clippers.

3.   Non Linear wave shaping – Clampers.

4.   Transistor as a switch.

5.   Study of Logic Gates & Some applications.

6.   Study of Flip-Flops & some applications.

7.   Sampling Gates.

8.   Astable Multivibrator.

9.   Monostable Multivibrator.

10.   Bistable Multivibrator.

11.   Schmitt Trigger.

12.   UJT Relaxation Oscillator.

13.   Bootstrap sweep circuit.

EQUIPMENT REQUIRED FOR LABORATORY:

1.   RPS - 0 – 30 V

2.   CRO - 0 – 20 M Hz.

3.   Function Generators - 0 – 1 M Hz

4.   Components

5.   Multi Meters

III Year – I SEMESTER

T	P	C
0	3	2

LIC APPLICATIONS LAB

Minimum Twelve Experiments to be conducted :

1. Study of ICs – IC 741, IC 555, IC 565, IC 566, IC 1496 – functioning, parameters and Specifications.
2. OP AMP Applications – Adder, Subtractor, Comparator Circuits.
3. Integrator and Differentiator Circuits using IC 741.
4. Active Filter Applications – LPF, HPF (first order)
5. Active Filter Applications – BPF, Band Reject (Wideband) and Notch Filters.
6. IC 741 Oscillator Circuits – Phase Shift and Wien Bridge Oscillators.
7. Function Generator using OP AMPs.
8. IC 555 Timer – Monostable Operation Circuit.
9. IC 555 Timer – Astable Operation Circuit.
10. Schmitt Trigger Circuits – using IC 741 and IC 555.
11. IC 565 – PLL Applications.
12. IC 566 – VCO Applications.
13. Voltage Regulator using IC 723.
14. Three Terminal Voltage Regulators – 7805, 7809, 7912.
15. 4 bit DAC using OP AMP.

EQUIPMENT REQUIRED FOR LABORATORIES:
1. RPS
2. CRO
3. Function Generator
4. Multi Meters
5. IC Trainer Kits (Optional)
6. Bread Boards
7. Components:-   IC741, IC555, IC565, IC1496, IC723, 7805, 7809, 7912 and other essential components.
8. Analog IC Tester

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III Year – I SEMESTER

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Digital System Design & DICA Laboratory

The students are required to design and draw the internal structure of the following Digital Integrated Circuits and to develop VHDL source code, perform simulation using relevant simulator and analyze the obtained simulation results using necessary synthesizer. Further, it is required to verify the logic with necessary hardware.

List of Experiments:

1. Realization of Logic Gates
2. 3 to 8 Decoder- 74138
3. 8*1 Multiplexer-74151 and 2*1 De-multiplexer-74155
4. 4-Bit Comparator-7485.
5. D Flip-Flop- 7474
6. Decade Counter- 7490
7. 4 Bit Counter-7493
8. Shift Register-7495
9. Universal shift register-74194/195
10. Ram (16*4)-74189 (read and write operations)
11. ALU

Equipment Required:

1. Xilinix ISE software-latest version
2. Personal computer with necessary pheripherals
3. Hardware kits- Various FPGA families.

III Year – I SEMESTER

T	**P**	**C**
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INTELLECTUAL PROPERTY RIGHTS AND PATENTS

Unit I

Introduction to Intellectual Property Law – Evolutionary past – Intellectual Property Law Basics - Types of Intellectual Property - Innovations and Inventions of Trade related Intellectual Property Rights – Agencies Responsible for Intellectual Property Registration – Infringement - Regulatory – Over use or Misuse of Intellectual Property Rights - Compliance and Liability Issues.

Unit II

Introduction to Copyrights – Principles of Copyright – Subject Matters of Copyright – Rights Afforded by Copyright Law –Copyright Ownership – Transfer and Duration – Right to Prepare Derivative Works –Rights of Distribution – Rights of performers **–** Copyright Formalities and Registration – Limitations – Infringement of Copyright – International Copyright Law- Semiconductor Chip Protection Act.

Unit III

Introduction to Patent Law – Rights and Limitations – Rights under Patent Law – Patent Requirements – Ownership and Transfer – Patent Application Process and Granting of Patent – Patent Infringement and Litigation – International Patent Law – Double Patenting – Patent Searching – Patent Cooperation Treaty – New developments in Patent Law- Invention Developers and Promoters.

Unit IV

Introduction to Trade Mark – Trade Mark Registration Process – Post registration procedures – Trade Mark maintenance – Transfer of rights – Inter parties Proceedings – Infringement – Dilution of Ownership of Trade Mark – Likelihood of confusion – Trade Mark claims – Trade Marks Litigation – International Trade Mark Law

Unit V

Introduction to Trade Secrets – Maintaining Trade Secret – Physical Security – Employee Access Limitation – Employee Confidentiality Agreement –

Trade Secret Law – Unfair Competition – Trade Secret Litigation – Breach of Contract – Applying State Law.

Unit VI

Introduction to Cyber Law – Information Technology Act - Cyber Crime and E-commerce – Data Security – Confidentiality – Privacy - International aspects of Computer and Online Crime.

REFERENCE BOOKS:

1. Deborah E.Bouchoux: "Intellectual Property". Cengage learning , New Delhi

2. Kompal Bansal & Parishit Bansal "Fundamentals of IPR for Engineers", BS Publications (Press)

3. Cyber Law. Texts & Cases, South-Western's Special Topics Collections

4. Prabhuddha Ganguli: ' Intellectual Property Rights" Tata Mc-Graw – Hill, New Delhi

5. Richard Stim: "Intellectual Property", Cengage Learning, New Delhi.

6.   R. Radha Krishnan, S. Balasubramanian: "Intellectual Property Rights",Excel Books. New Delhi.

7. M. Ashok Kumar and Mohd.Iqbal Ali: "Intellectual Property Right" Serials Pub.